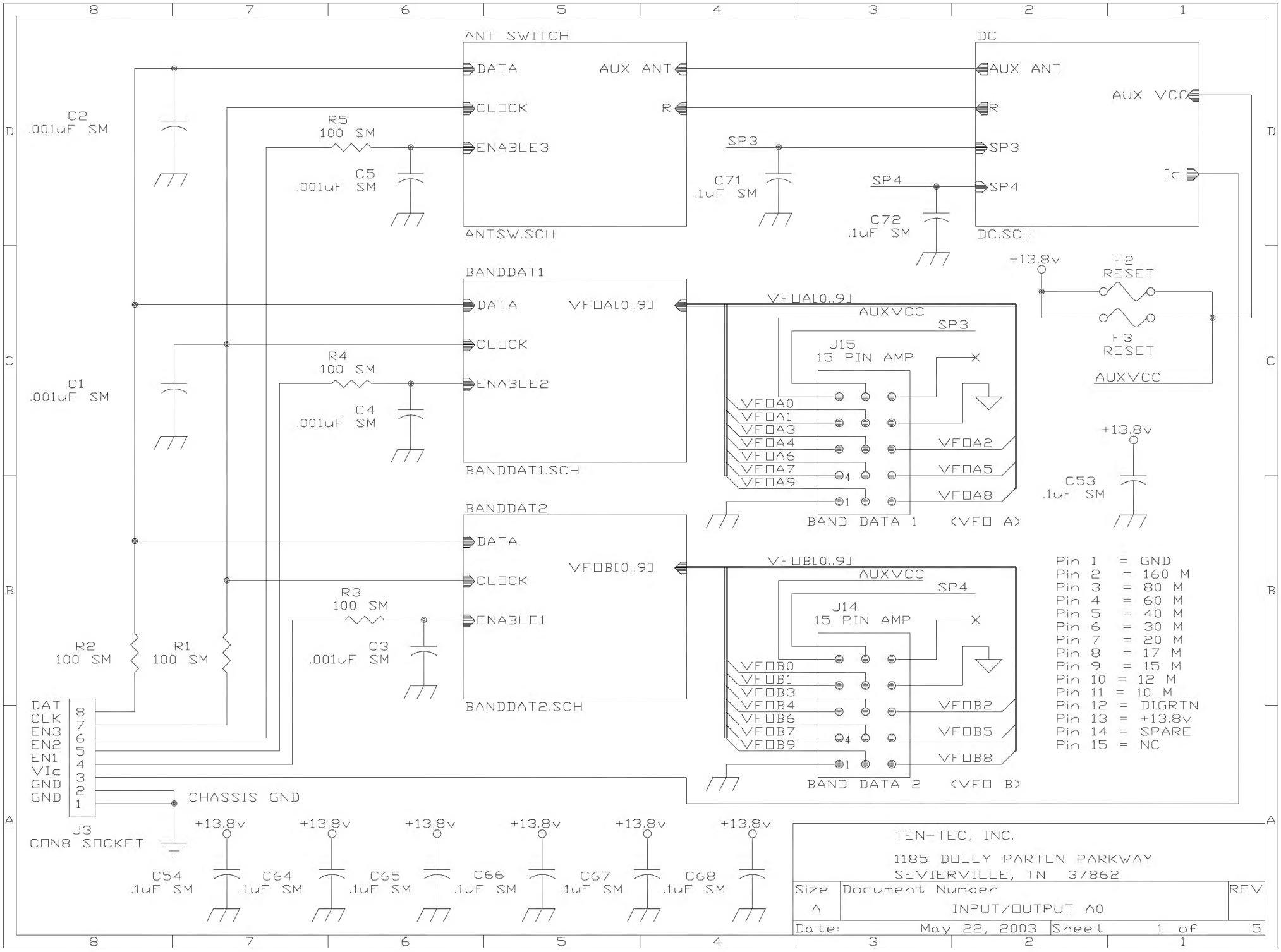
ANT SWITCH
DATA
CLOCK
ENABLE3
AUX ANT
R
ANTSW.SCH
DC
AUX ANT
R
SP3
SP4
AUX VCC
Ic
DC.SCH
C2
.001uF SM
R5
100 SM
C5
.001uF SM
C71
.1uF SM
C72
.1uF SM
+13.8v
F2
RESET
F3
RESET
AUXVCC
BANDDAT1
DATA
CLOCK
ENABLE2
VFOA[0..9]
BANDDAT1.SCH
C1
.001uF SM
R4
100 SM
C4
.001uF SM
J15
15 PIN AMP
VFOA0
VFOA1
VFOA3
VFOA4
VFOA6
VFOA7
VFOA9
VFOA2
VFOA5
VFOA8
BAND DATA 1 (VFO A)
C53
.1uF SM
BANDDAT2
DATA
CLOCK
ENABLE1
VFOB[0..9]
BANDDAT2.SCH
R3
100 SM
C3
.001uF SM
R2
100 SM
R1
100 SM
J14
15 PIN AMP
VFOB0
VFOB1
VFOB3
VFOB4
VFOB6
VFOB7
VFOB9
VFOB2
VFOB5
VFOB8
BAND DATA 2 (VFO B)
Pin 1 = GND
Pin 2 = 160 M
Pin 3 = 80 M
Pin 4 = 60 M
Pin 5 = 40 M
Pin 6 = 30 M
Pin 7 = 20 M
Pin 8 = 17 M
Pin 9 = 15 M
Pin 10 = 12 M
Pin 11 = 10 M
Pin 12 = DIGRTN
Pin 13 = +13.8v
Pin 14 = SPARE
Pin 15 = NC
DAT
CLK
EN3
EN2
EN1
VIc
GND
GND
J3
CON8 SOCKET
CHASSIS GND
C54 .1uF SM
C64 .1uF SM
C65 .1uF SM
C66 .1uF SM
C67 .1uF SM
C68 .1uF SM
TEN-TEC, INC.
1185 DOLLY PARTON PARKWAY
SEVIERVILLE, TN 37862
Size A
Document Number
INPUT/OUTPUT A0
REV
Date: May 22, 2003
Sheet 1 of 5